

A1 cell increases, a switching time 'a' required for applying the data signals is increased. Thus, a charging time 'b' required for charging the data signals into the liquid crystal cell is shortened.

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Please replace paragraph [0013] with the following:

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A2 [0013] However, in the dot inversion system, if positive (+) data signals are applied to the liquid crystal cells at odd-numbered frames, negative (-) data signals are applied to the liquid crystal cells at even-numbered frames. Accordingly, a level for switching the data signal is increased since the data signals applied to the liquid crystal cells at two consecutive frames should be converted from the positive (+) polarity to the negative (-) polarity, thereby increasing the switching time 'a' of the data signal. As a result, since a time 'c' at which a gate pulse GP is applied is fixed for each resolution, and a switching time 'a' of the data signal is increased, a time 'b' at which the data signal is applied to the liquid crystal cell should be decreased. Accordingly, the data signal is not completely charged in the liquid crystal cell, thereby distorting color or brightness of the image.

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Please replace paragraph [0035] with the following:

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B3 [0035] The data driving IC 8 may include shift registers and latches. The data driving IC 8 shifts data bits in response to a data shift clock DSC, and applies data for the data lines DL1 to DLn simultaneously in response to a data output enable signal DOE.

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